**DIGITAL ELECTRONICS PROJECT REPORT**

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**Thus far:**

1. **We have made a 4\*1 MUX**
2. **We also made a 8\*1 MUX coupled with a clock generator for the input.**

**We have first implemented with a toggle to check its working and then proceeded to give random signals and one DC signal as input signal with Clock frequency of select lines 400,200,100 mHz.**

1. **We also understood how to use Proteus Software with working of MUX and De-MUX.**

**The file is attached below:**

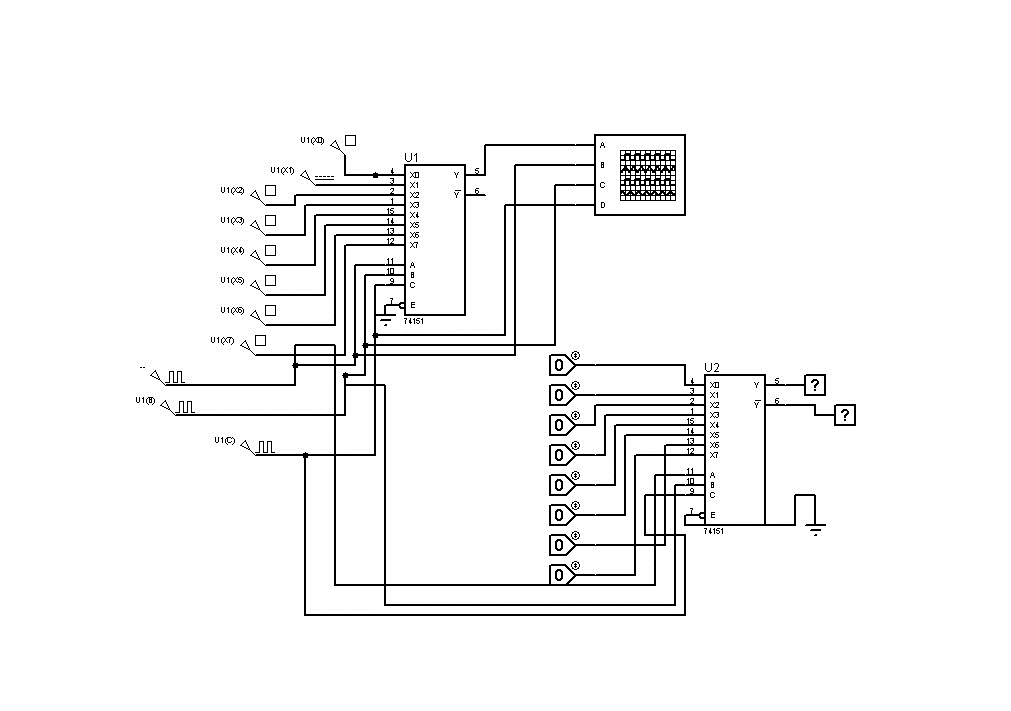
**The yellow line is output line while the blue, red, green are the clock frequency which are being viewed in the oscilloscope of proteus.**

**The mux used here is a 74151 IC.**

**Next step:**

**1.To setup a De-mux coupled with the mux to verify the random signals**

**2.Find the causes of delay by changing the frequency of clock signals to select line.**



A computer screen with a colorful line on it

Description automatically generated